



**T<sub>c</sub> =25**

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current	I <sub>D@TC=25</sub>	85	A
	I <sub>D@TC=75</sub>	64.6	A
	I <sub>D@TC=100</sub>	53.6	A
Pulsed Drain Current	I <sub>DM</sub>	190	A
Total Power Dissipation	P <sub>D@TC=25</sub>	60	W
Total Power Dissipation	P <sub>D@TA=25</sub>	2.0	W

**Thermal resistance**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	$R_{thJC}$	-	-	2.1	$^{\circ}C/W$
Thermal resistance, junction - ambient	$R_{thJA}$	-	-	62.5	$^{\circ}C/W$
Soldering temperature, wavesoldering for 10s	$T_{sold}$	-	-	265	$^{\circ}C$

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	60			V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2		2.5	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=60V, V_{GS}=0V$			1.0	$\mu A$
Gate- Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 20V, V_{DS}=0V$			100	nA
Static Drain-source On Resistance		$V_{GS}=10V, I_D=20A$				
		$V_{GS}=4.5V, I_D=10A$				
Forward Transconductance	$g_{FS}$	$V_{DS}=25V, I_D=10A$				
Source-drain voltage	$V_{SD}$	$I_S=20A$				

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	$C_{iss}$	$f = 1MHz$	-	6510	-	pF
Output capacitance	$C_{oss}$		-	450	-	
Reverse transfer capacitance	$C_{rss}$		-	200	-	

**Gate Charge characteristics( $T_a = 25$  )**

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Total gate charge	$Q_g$	$V_{DD}=25V$	-	54	-	nC
Gate - Source charge	$Q_{gs}$	$I_D = 15A$	-	18	-	
Gate - Drain charge	$Q_{gd}$	$V_{GS} = 10V$	-	21	-	

Note:

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Fig.1 Power Dissipation

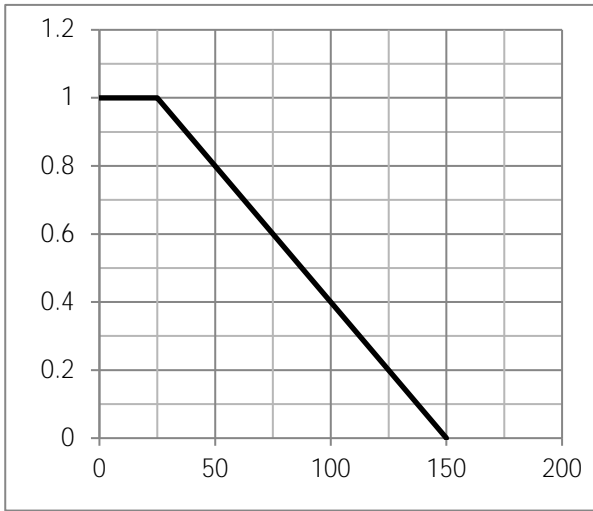


Fig.2 Typical output Characteristics

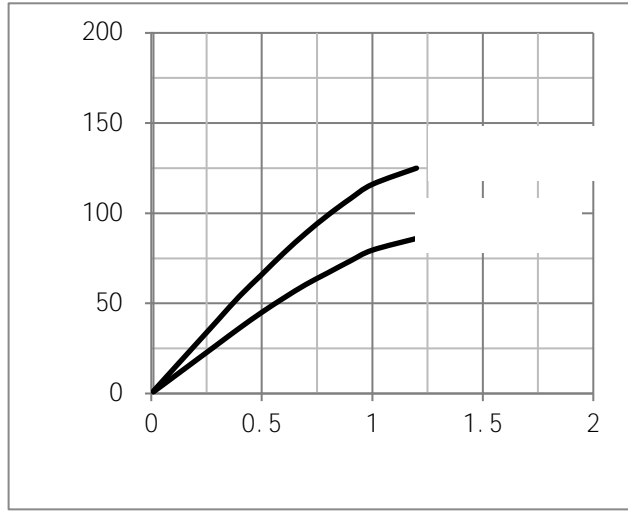


Fig.3 Threshold Voltage V.S Junction Temperature

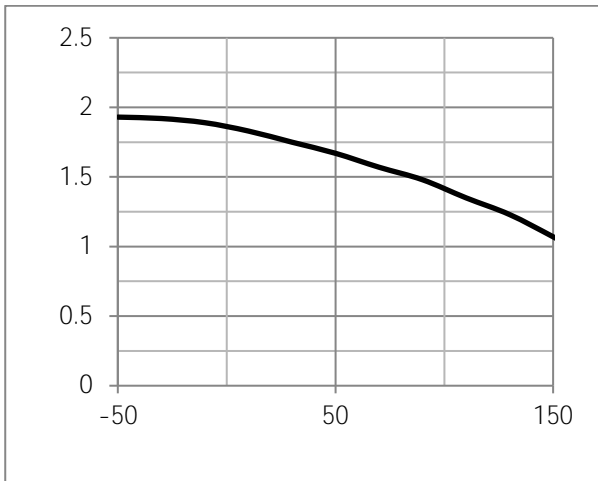


Fig.4 Resistance V.S Drain Current

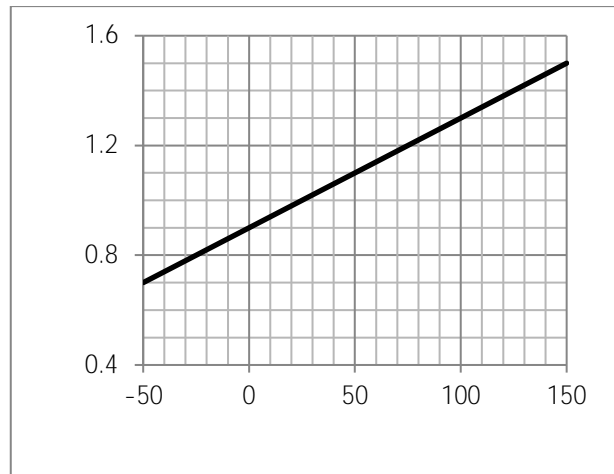
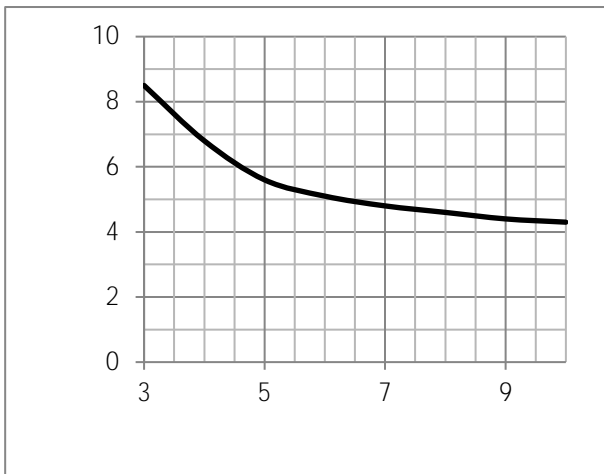
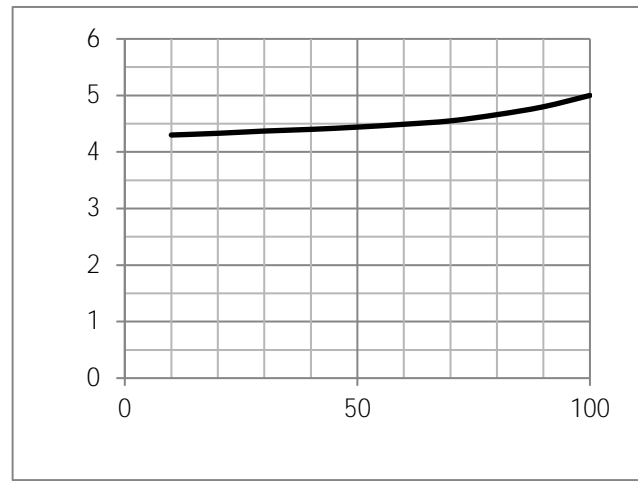


Fig.7 Switching Time Measurement Circuit

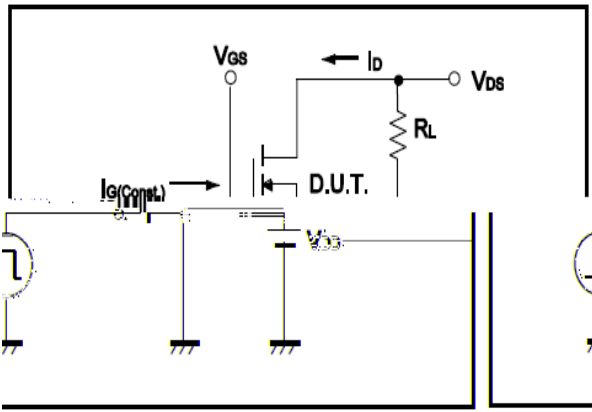


Fig.8 Gate Charge Waveform

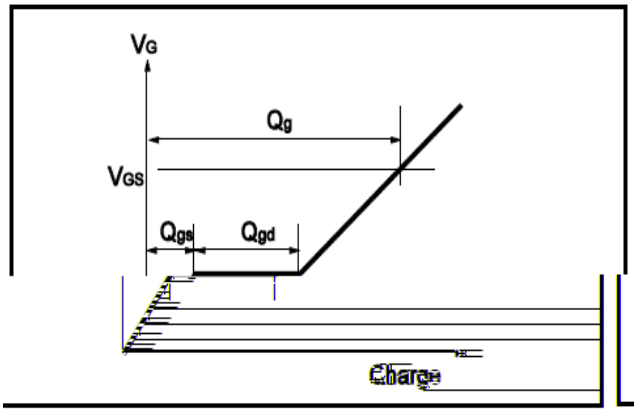


Fig.9 Switching Time Measurement Circuit

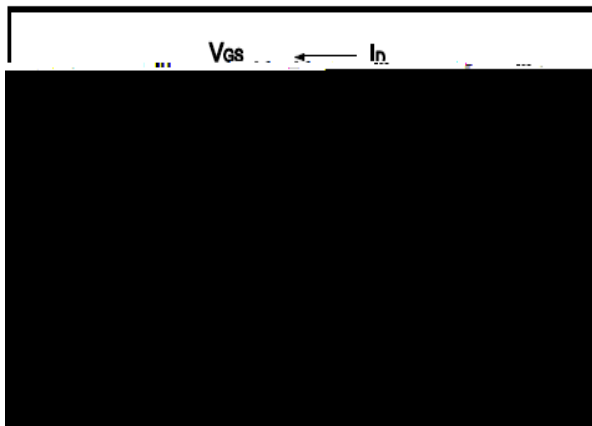


Fig.10 Gate Charge Waveform

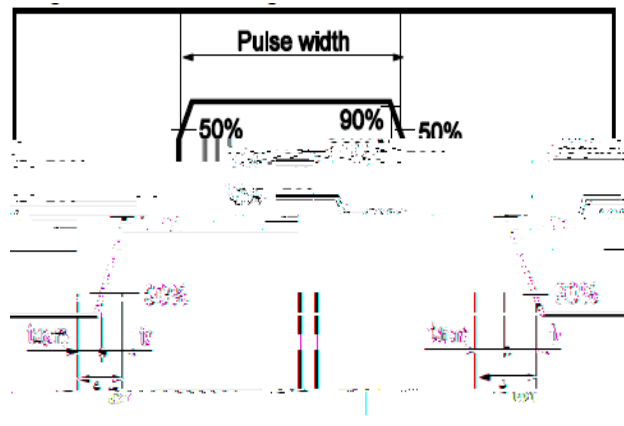


Fig.11 Avalanche Measurement Circuit

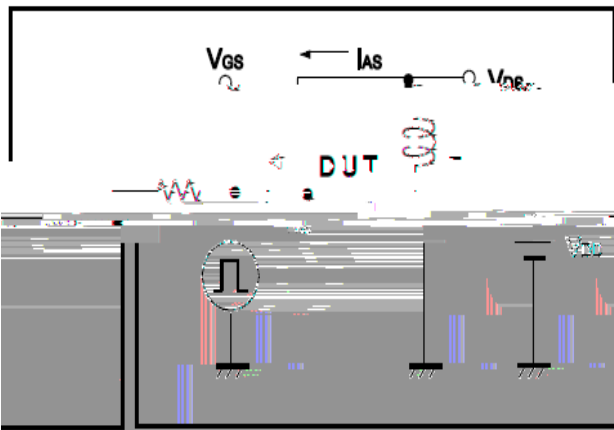


Fig.12 Avalanche Waveform

