

**Thermal resistance**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}	-	-	7.3	v C/W
Thermal resistance, junction - ambient	R_{thJA}	-	-	62	v C/W
Soldering temperature, wavesoldering for 10s	T_{sold}	-	-	265	v C

N Channel Absolute Maximum Ratings $T_C = 25$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	20	V

Pulsed Drain Current	I_{DM}	45	A
Total Power Dissipation	$P_D@T_C=25$	17	W
Total Power Dissipation	$P_D@T_A=25$	2.0	W
Operating Junction Temperature	T_J	-55 to 150	
Storage Temperature	T_{STG}	-55 to 150	
Single Pulse Avalanche Energy	E_{AS}	6	mJ

P Channel Absolute Maximum Ratings $T_C = 25$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_D@T_C = 25$	-14	A
	$I_D@T_C = 75$	-10.6	A
	$I_D@T_C = 100$	-8.8	A
Pulsed Drain Current	I_{DM}	-42	A
Total Power Dissipation	$P_D@T_C=25$	17	W
Total Power Dissipation	$P_D@T_A=25$	2.0	W
Operating Junction Temperature	T_J	-55 to 150	

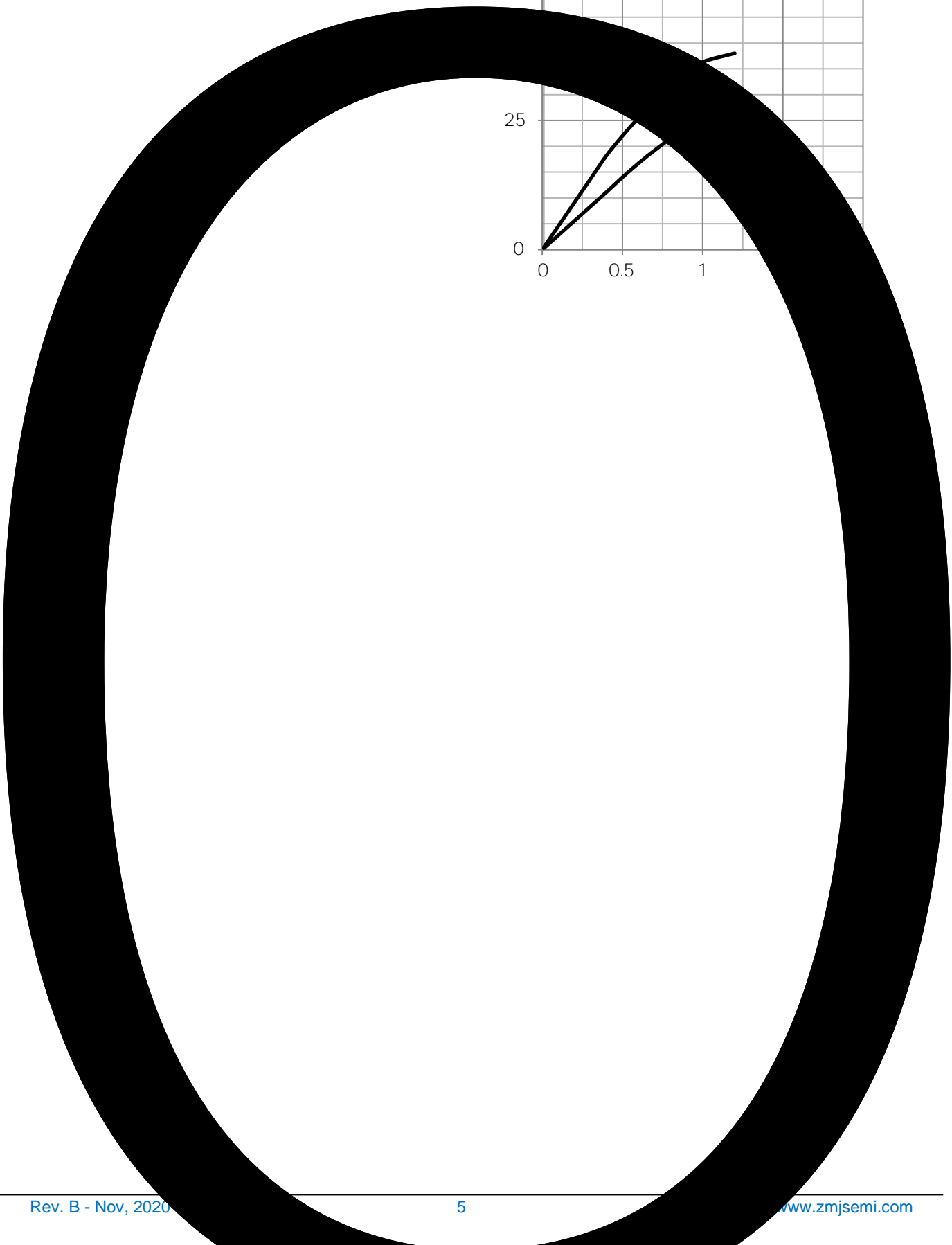
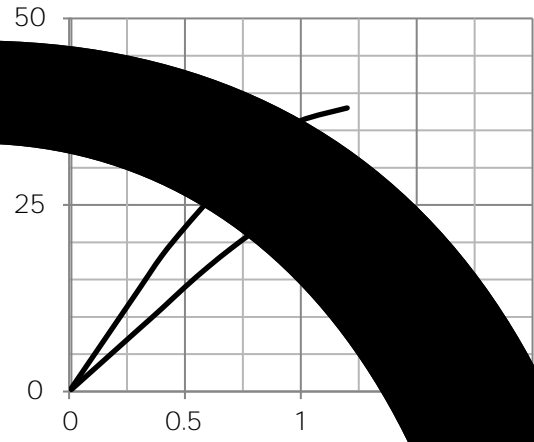
Gate Resistance	Rg	f = 1MHz		1.5		
Input capacitance	Ciss	f = 1MHz V _{DS} =25V	-	280	-	pF
Output capacitance	Coss		-	46	-	
Reverse transfer capacitance	Crss		-	25	-	
Total gate charge	Qg	V _{DD} = 25V I _D = 5A V _{GS} = 10V	-	5.3	-	nC
Gate - Source charge	Qgs		-	1.4	-	
Gate - Drain charge	Qgd		-	0.8	-	

P Channel Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = -250uA	-30			V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} = V _{DS} , I _D = -250uA	-1.2		-2.5	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} = -30V, V _{GS} = 0V			-1.0	uA
Gate- Source Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			...100	nA
Static Drain-source On Resistance		V _{GS} = -10V, I _D = -6A		30	42	m
		V _{GS} = -4.5V, I _D = -4A		44	60	m
Forward Transconductance	g _{FS}	V _{DS} = -10V, I _D = -5A		1.5		S

Dynamic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Gate Resistance	Rg	f = 1MHz		10		
Input capacitance	Ciss	f = 1MHz V _{DS} =25V	-	850	-	pF
Output capacitance	Coss		-	125	-	
Reverse transfer capacitance	Crss		-	115	-	
Total gate charge	Qg	V _{DD} = 25V I _D = 5A V _{GS} = 10V	-	12	-	nC
Gate - Source charge	Qgs		-	5	-	
Gate - Drain charge	Qgd		-	6	-	



Test Circuit

Fig.1 Switching Time Measurement Circuit

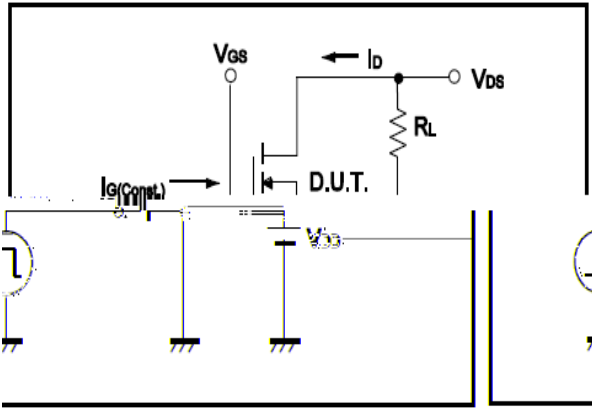


Fig.2 Gate Charge Waveform

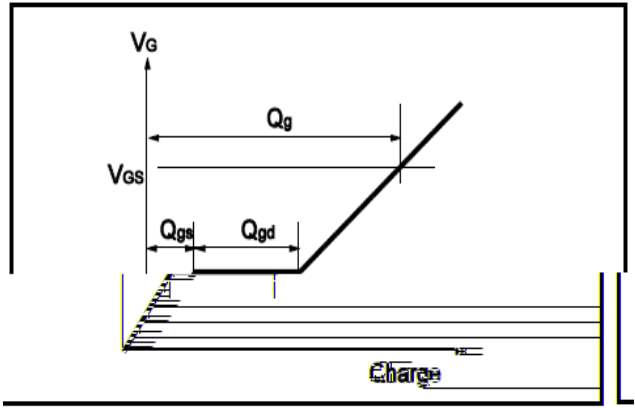


Fig.3 Switching Time Measurement Circuit

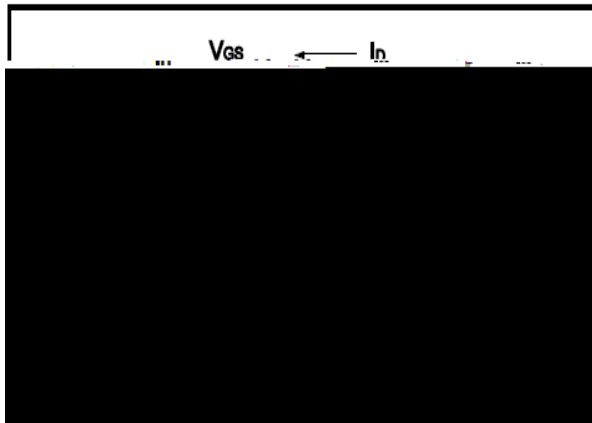


Fig.4 Gate Charge Waveform

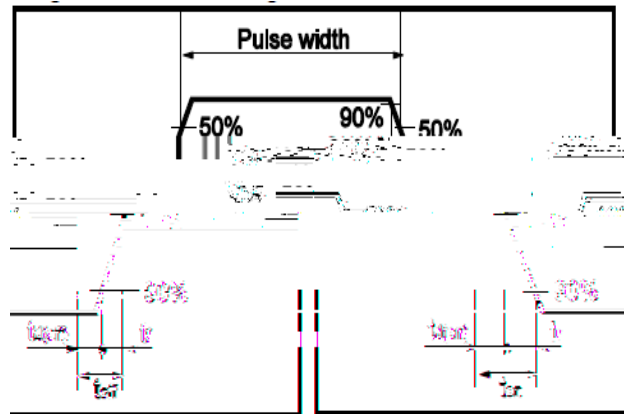


Fig.5 Avalanche Measurement Circuit

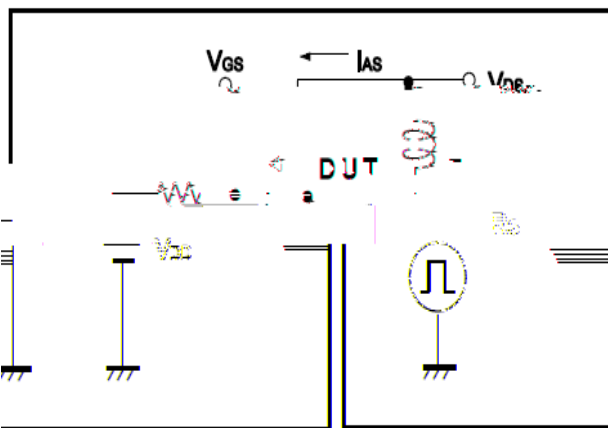
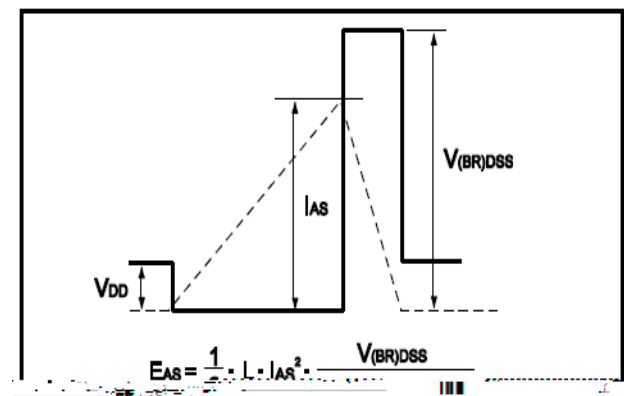


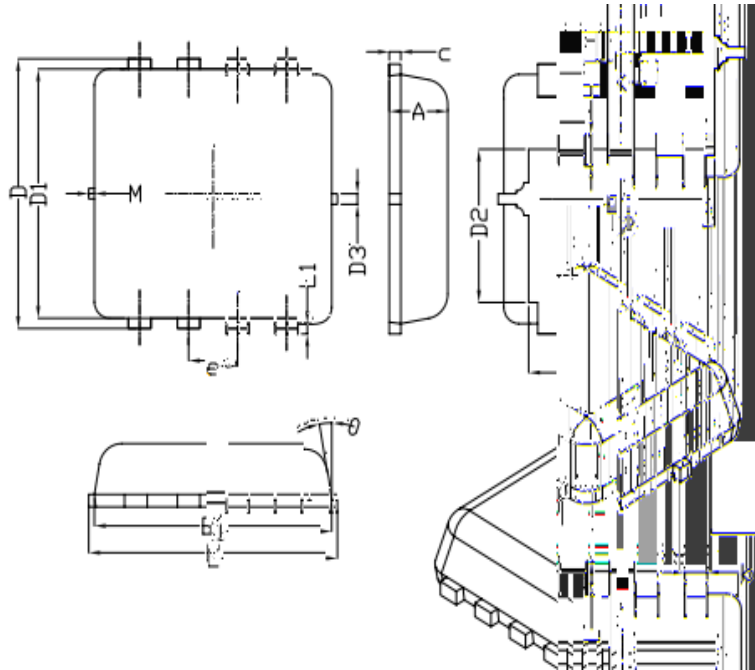
Fig.6 Avalanche Waveform





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Unit mm



SYMBOL	DIMENSIONAL REQMTS		
	MIN	NOM	MAX
A	0.70	0.75	0.80
B	0.35	0.25	0.25
C	0.15	0.25	0.25
D	0.30	0.25	0.25
D1	0.30	0.25	0.25
D2	0.30	0.25	0.25
D3	0.30	0.25	0.25
L1	0.30	0.25	0.25
L2	0.30	0.25	0.25
L3	0.30	0.25	0.25
L4	0.30	0.25	0.25
L5	0.30	0.25	0.25
L6	0.30	0.25	0.25
L7	0.30	0.25	0.25
L8	0.30	0.25	0.25
L9	0.30	0.25	0.25
L10	0.30	0.25	0.25
L11	0.30	0.25	0.25
L12	0.30	0.25	0.25
L13	0.30	0.25	0.25
L14	0.30	0.25	0.25
L15	0.30	0.25	0.25
L16	0.30	0.25	0.25
L17	0.30	0.25	0.25
L18	0.30	0.25	0.25
L19	0.30	0.25	0.25
L20	0.30	0.25	0.25
L21	0.30	0.25	0.25
L22	0.30	0.25	0.25
L23	0.30	0.25	0.25
L24	0.30	0.25	0.25
L25	0.30	0.25	0.25
L26	0.30	0.25	0.25
L27	0.30	0.25	0.25
L28	0.30	0.25	0.25
L29	0.30	0.25	0.25
L30	0.30	0.25	0.25
L31	0.30	0.25	0.25
L32	0.30	0.25	0.25
L33	0.30	0.25	0.25
L34	0.30	0.25	0.25
L35	0.30	0.25	0.25
L36	0.30	0.25	0.25
L37	0.30	0.25	0.25
L38	0.30	0.25	0.25
L39	0.30	0.25	0.25
L40	0.30	0.25	0.25
L41	0.30	0.25	0.25
L42	0.30	0.25	0.25
L43	0.30	0.25	0.25
L44	0.30	0.25	0.25
L45	0.30	0.25	0.25
L46	0.30	0.25	0.25
L47	0.30	0.25	0.25
L48	0.30	0.25	0.25
L49	0.30	0.25	0.25
L50	0.30	0.25	0.25